

L Number	Hits	Search Text	DB	Time stamp
1	2	((("6044225") or ("5379384")).PN.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 19:47
2	11354	SDRAM or (synchronous adj dynamic adj random adj access)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 19:48
3	3445	rambus	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 19:48
4	5533	DDR or (double adj data adj rate)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 19:49
5	25814	PCI or (peripheral adj component adj interconnect)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 19:49
6	19543	SCSI or (small adj computer adj system adj interface)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 19:51
7	3037	low adj power adj mode	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 19:51
8	32772	memory adj controller	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 19:53
9	3648	configuration adj register	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 19:53
10	2805	register adj block	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 19:54
11	8951	memory adj map	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 19:54
12	4020	fan-out	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 19:54
13	3043	select adj bit	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 19:56
14	5166	SCSI with controller	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 19:57
15	3	tree adj hierarchical adj configuration	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 19:59
16	0	((("6044225") or ("5379384")).PN.) and (PCI or (peripheral adj component adj interconnect)) and (SCSI or (small adj computer adj system adj interface)) and (low adj power adj mode)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 19:59

20	130	(memory adj controller) and (configuration adj register) and (register adj block)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 20:00
22	48	(memory adj map) and ((memory adj controller) and (configuration adj register) and (register adj block))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 20:00
18	1	rambus and (PCI or (peripheral adj component adj interconnect)) and (SCSI or (small adj computer adj system adj interface)) and (low adj power adj mode)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 20:01
19	5	(DDR or (double adj data adj rate)) and (PCI or (peripheral adj component adj interconnect)) and (SCSI or (small adj computer adj system adj interface)) and (low adj power adj mode)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 20:01
23	1	fan-out and ((memory adj map) and ((memory adj controller) and (configuration adj register) and (register adj block)))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 20:02
24	1	fan-out and ((SDRAM or (synchronous adj dynamic adj random adj access)) and (PCI or (peripheral adj component adj interconnect)) and (SCSI or (small adj computer adj system adj interface)) and (low adj power adj mode))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 20:02
21	3	((SDRAM or (synchronous adj dynamic adj random adj access)) and (PCI or (peripheral adj component adj interconnect)) and (SCSI or (small adj computer adj system adj interface)) and (low adj power adj mode)) and ((DDR or (double adj data adj rate)) and (PCI or (peripheral adj component adj interconnect)) and (SCSI or (small adj computer adj system adj interface)) and (low adj power adj mode))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 20:05
25	4	(low adj power adj mode) and fan-out	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 20:06
17	19	(SDRAM or (synchronous adj dynamic adj random adj access)) and (PCI or (peripheral adj component adj interconnect)) and (SCSI or (small adj computer adj system adj interface)) and (low adj power adj mode)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 20:19
26	699	(711/173).CCLS.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 20:19
27	1221	(711/167).CCLS.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 20:20
28	1305	(711/170).CCLS.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 20:20
29	1599	(711/154).CCLS.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/09/05 20:20